SEARCH REQUEST FORM
Scientific and Technical Information Center

Requester's Full Name: _P. Laufer_   Examiner# : _73139_   Date: _6/13/02_

Art Unit: _____   Phone Number: _30_   Serial Number: _10/006785_

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****

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc., if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: _____

Inventors (please provide full names): _____

_____

Earliest Priority Filing Date: _____

For Sequence Searches Only Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.

Litigation
5,983314

06-13-02 P05:16 IN

****

STAFF USE ONLY

Searcher: _A. Green_

Searcher Phone: _6-4767_

Searcher Location: _4B40_

Date Searcher Picked Up: _6/13/02_

Date Completed: _6/13/02_

Searcher Prep & Review Time: _.5_

Clerical Prep Time: _____

Online Time: _10_

Type of search

NA Sequence (#) _____

AA Sequence (#) _____

Structure (#) _____

Bibliographic _____

Litigation ✓

Full Text _____

Patent Family _____

Other _____

Vendors and cost where applicable

(STN) _____

Dialog _____

(Questel/Orbit) _31.21_

Dr. Link _____

(Lexis/Nexis) _____

Sequence System _____

(WWW/Internet) _____

Other (specify) _____

From:	Laufer, Pinchus
Sent:	Tuesday, June 11, 2002 6:04 PM
To:	Green, Shirelle
Subject:	litigation searches

Please generate searches for:

(1) US Patent No 5,944,749   Re. S.N. 09/945,357   Inventors: Charles Smoot et al.
     Owner of Record: Samsung Electronics   (Appeared in OG 6/04/02)

(2) US Patent No 5,978,833   Re. S.N. 09/944,786   Inventors: Richard Pashley et al.
     Owner of Record: INTEL   (Appeared in OG 6/04/02)

(3) US Patent No 5,983,314   Re. S.N. 10/006,785   Inventors: Todd Merritt
     Owner of Record: MICRON Technology   (Appeared in OG 6/04/02)

No Rush

Thank You,

Pinchus

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5,983,314

GET 1st DRAWING SHEET OF 11

Nov. 9, 1999

Output buffer having inherently precise data
masking

REISSUE: Reissue Application filed Nov. 9, 2001 (O.G. Jun. 4, 2002) Ex.
Gp.: 2186; Re. S.N. 10/006,785

   INVENTOR: Merritt, Todd A., Boise, Idaho

   ASSIGNEE-AT-ISSUE: Micron Technology, Inc., Boise, Idaho (02)

   APPL-NO: 898,177

   FILED: Jul. 22, 1997

   CERTCORR: May 22, 2001 a Certificate of Correction was issued for
this patent (O.G. May 22, 2001)

   INT-CL: [6] G06F 13#00; G06F 3#00

   US-CL: 711#105; 711#104; 710#52; 710#129; 710#130; 710#49; 710#262;
365#120; 365#189.01; 365#189.05; 365#190; 365#233; 365#202

   SEARCH-FLD: 365#120, 189.05, 190, 233, 189.01, 202; 711#105, 106,
104, 109-110; 710#49, 52, 129-130, 131, 262

   PRIM-EXMR: Cabeca, John W.

   ASST-EXMR: Tran, Denise

   LEGAL-REP: Seed and Berry LLP

No Documents Found

No documents were found for your search (**5,983,314 or 5983314**). Please edit your search and try again. You may want to try one or more of the following:

- Check for spelling errors.
- Remove some search terms.
- Use more common search terms.
- If applicable, look for all dates.

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File: CASES

No Documents Found

No documents were found for your search (**5,983,314 or 5983314**). Please edit your search and try again. You may want to try one or more of the following:

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LEXIS-NEXIS
Library: PATENT
File: JNLS

?us5983314/pn

** SS 3: Results 1

Search statement   4

?prt full nonstop legalall

1/1 PLUSPAT - (C) QUESTEL-ORBIT
PN - US5983314 A 19991109 [US5983314]
TI - (A) Output buffer having inherently precise data masking
PA - (A) MICRON TECHNOLOGY INC   (US)
IN - (A) MERRITT TODD A   (US)
AP - US89817797 19970722 [1997US-0898177]
PR - US89817797 19970722 [1997US-0898177]
IC - (A) G06F-003/00 G06F-013/00
EC - G11C-007/10R
PCL - ORIGINAL (O) : 711105000; CROSS-REFERENCE (X) : 365120000 365189010
      365189050 365190000 365202000 365233000 710049000 710052000
      710262000 711104000
DT - Basic
CT - US4899310; US5235693; US5381540; US5383154; US5394366; US5414379;
      US5661692; US5787457
STG - (A) United States patent
AB - A maskable data output buffer includes an output stage receiving data
      signals from a data coder. The signals output from the data coder are
      normally complementary data signals corresponding to complementary
      data input signals. However, in response to receiving a mask signal,
      the data coder forces the output signals to be other than
      complementary. The output stage normally generates a data output
      signal corresponding to the complementary data input signals. However,
      when the data input signals are other than complementary, the output
      of the output stage assumes a high impedance condition. Since the
      timing of the high impedance condition is determined from the data
      signals themselves, the timing of the mask operation is inherently
      properly timed to the output of the data from the data output buffer.

1/1 LGST - (C) LEGSTAT
PN - US 5983314 [US5983314]
AP - US 898177/97 19970722 [1997US-0898177]
DT - US-P
ACT - 19970722 US/AE-A
      APPLICATION DATA (PATENT)
      {US 898177/97 19970722 [1997US-0898177]}
    - 19991109 US/A
      PATENT
    - 20010522 US/CC
      CERTIFICATE OF CORRECTION
UP - 2001-22

1/1 CRXX - (C) CLAIMS/RRX
PN - 5,983,314 A 19991109 [US5983314]
PA - Micron Technology Inc
ACT - 20011109 REISSUE REQUESTED
      ISSUE DATE OF O.G.: 20020604
      REISSUE REQUEST NUMBER: 10/006785
      EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2186

Reissue Patent Number:

1/2 PAST - (C) Thomson Derwent
AN - 200223-001830
PN - 5983314 A [US5983314]
OG - 2002-06-04
ACT - REISSUE APPLICATION FILED

2/2 PAST - (C) Thomson Derwent
AN - 200121-001029
PN - 5983314 A [US5983314]
OG - 2001-05-22
ACT - CERTIFICATE OF CORRECTION

?fam us5983314/pn

    1 Patent Groups
    ** SS 3: Results 1

    Search statement   4

?famstate nonstop

    1/1 INPADOC - (C) INPADOC
    PN   - US 5983314 A 19991109 [US5983314]
    TI   - OUTPUT BUFFER HAVING INHERENTLY PRECISE DATA MASKING
    IN   - MERRITT TODD A [US]
    PA   - MICRON TECHNOLOGY INC [US]
    AP   - US 898177/97-A 19970722 [1997US-0898177]
    PR   - US 898177/97-A 19970722 [1997US-0898177]
    IC   - G06F-013/00; G06F-003/00

    1/1 LEGALI - (C) LEGSTAT
    PN   - US 5983314 [US5983314]
    AP   - US 898177/97 19970722 [1997US-0898177]
    DT   - US-P
    ACTE- 19970722 US/AE-A
          APPLICATION DATA (PATENT)
          {US 898177/97 19970722 [1997US-0898177]}
       - 19991109 US/A
          PATENT
       - 20010522 US/CC
          CERTIFICATE OF CORRECTION
    UP   - 2001-22